

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Richard K. Williams; Michael E. Cornell; Wai Tien Chen
Assignee: Advanced Analogic Technologies, Inc.
Title: Modular Bipolar-CMOS-DMOS Analog Integrated Circuit And Power Technology
Application No.: 10/767,680 Filing Date: 01/28/2004
Examiner: Unknown Group Art Unit: 2812
Docket No.: AAT007-5C US Confirmation No: 7095

Santa Clara, California
August 8, 2005

COMMISSIONER FOR PATENTS
P.O. BOX 1450
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**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, §1.97 and §1.98, the Applicants submit for consideration in the above-identified patent application the documents listed on the accompanying Form PTO-892 (4 pages), which lists the references cited in the parent of this application, Application No. 10/262,567. The Examiner is requested to make each of the documents listed on the Form PTO-892 of record in this application.

This Information Disclosure Statement is submitted pursuant to 37 CFR §1.97(b) as it within three months of the filing date of this application and/or before the mailing of a first Office Action on the merits. Accordingly, no fee is required.

Applicants would appreciate the Examiner initialing each of the references listed on the enclosed Form PTO-892, indicating that the information has been considered and made of record herein.

The information contained in this Information Disclosure Statement is to the best of my knowledge and is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not

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exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.


In any fee is required for this Information Disclosure Statement, please charge the amount of such fee to Deposit Account No. 50-2263.

CERTIFICATE OF MAILING BY "FIRST CLASS"

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. BOX 1450, Alexandria, VA. 22313-1450 on the above date.

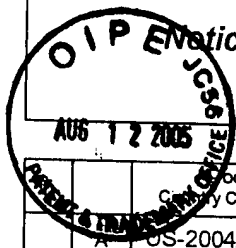

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Respectfully submitted,


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Notice of References Cited

Application/Control No.

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Applicant(s)/Patent Under
Reexamination
WILLIAMS ET AL.

Examiner

Mary Wilczewski

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	A	US-2004/0033666	02-2004	Williams et al.	438/297
	B	US-2004/0032005	02-2004	Williams et al.	257/510
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	C	US-5,777,362	07-1998	Pearce, Lawrence George	257/343
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	C	US-6,657,242	12-2003	Norstrom et al.	257/197
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